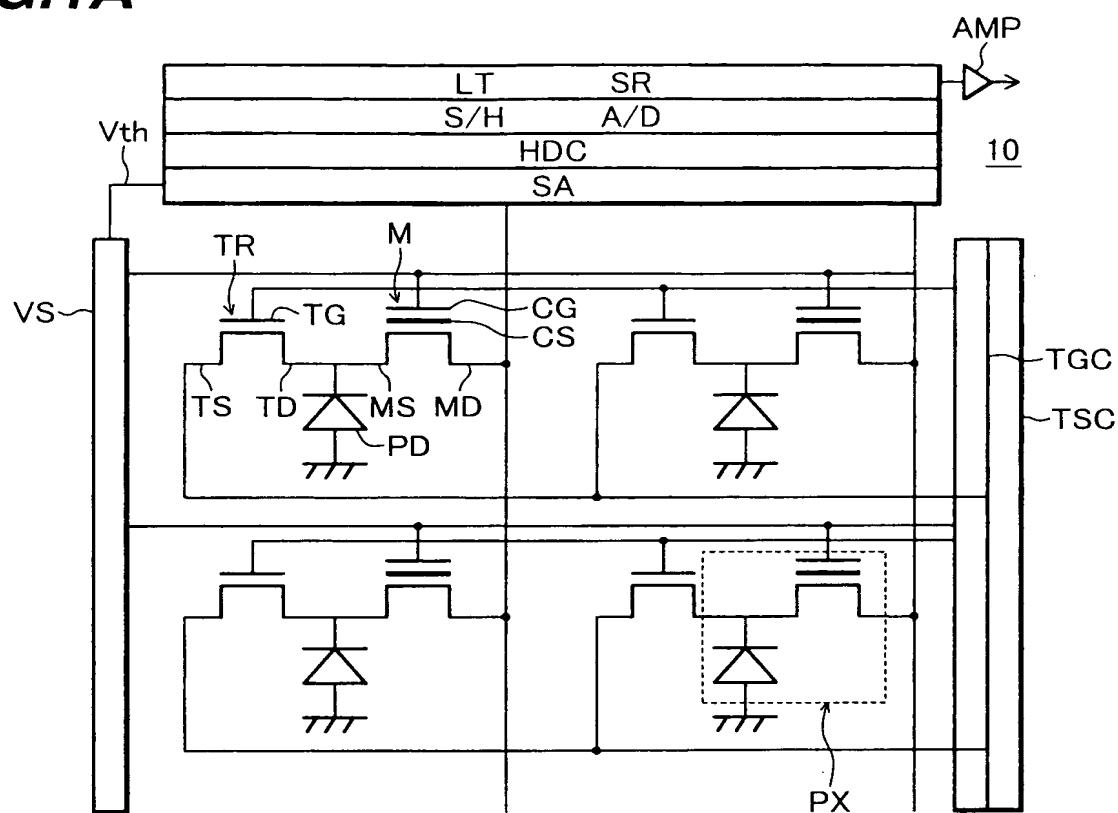
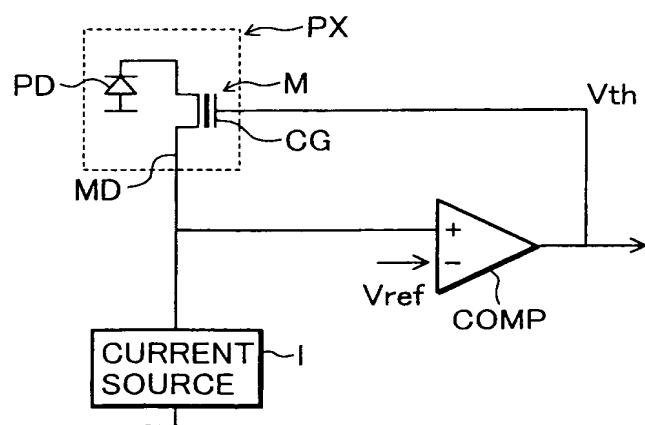


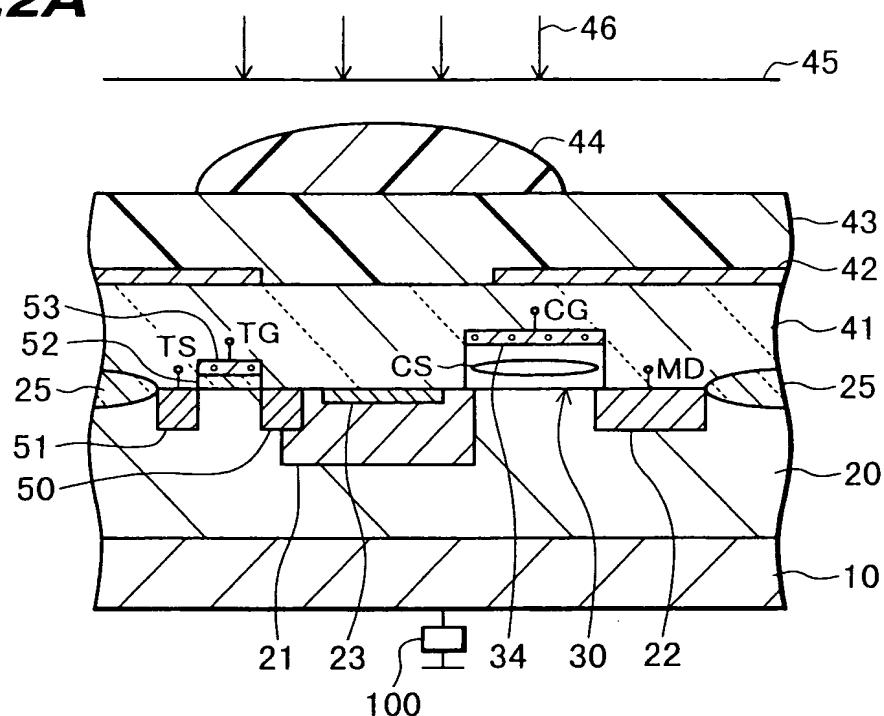
**FIG. 1A**



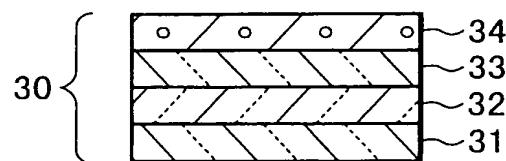
**FIG. 1B**



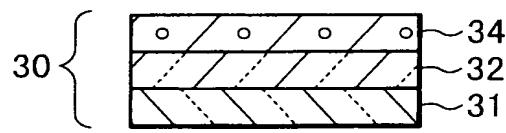
**FIG.2A**



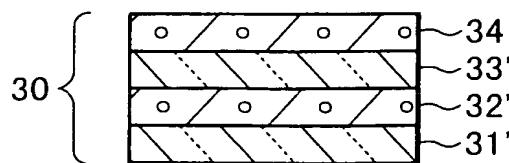
**FIG.2B**



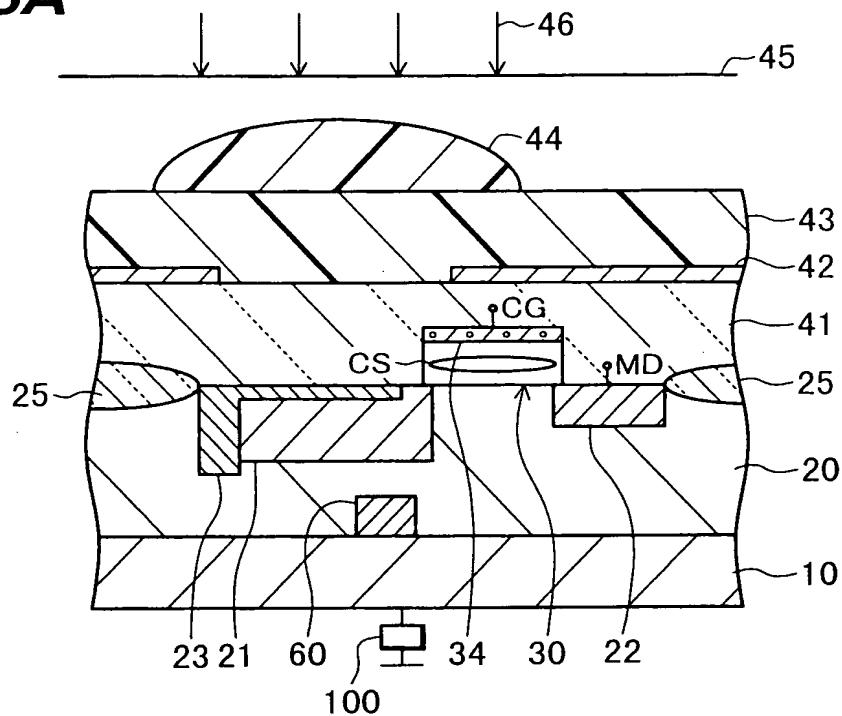
**FIG.2C**



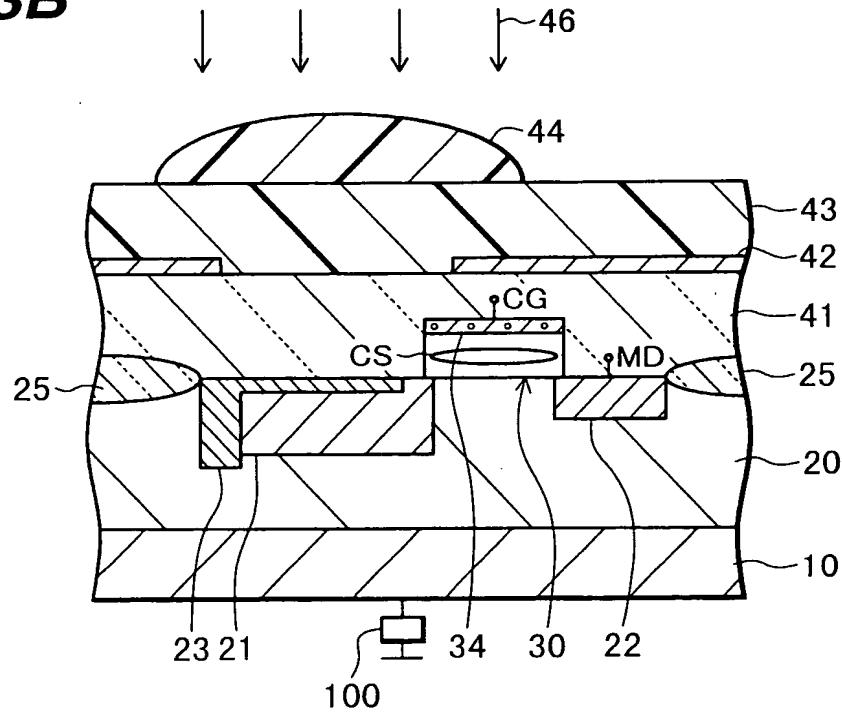
**FIG.2D**



**FIG.3A**

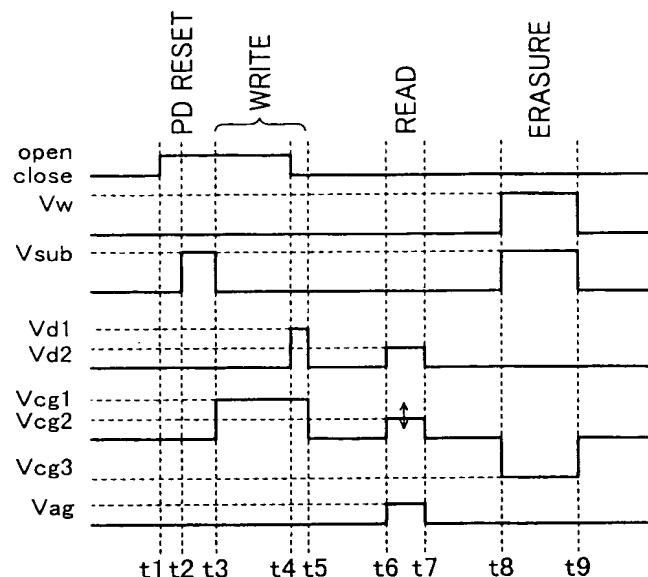


**FIG.3B**



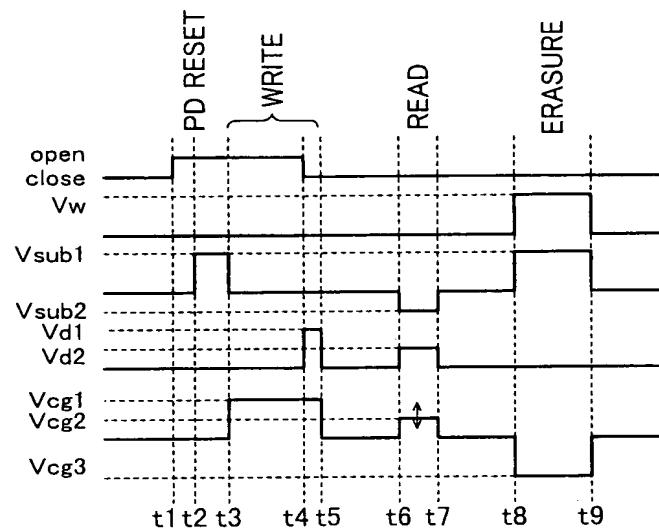
**FIG.4A**

MECHANICAL SHUTTER  
 p-TYPE WELL  
 n-TYPE SUBSTRATE  
 MEMORY ELEMENT DRAIN MD  
 MEMORY ELEMENT CONTROL GATE CG  
 AUXILIARY TRANSISTOR GATE TG



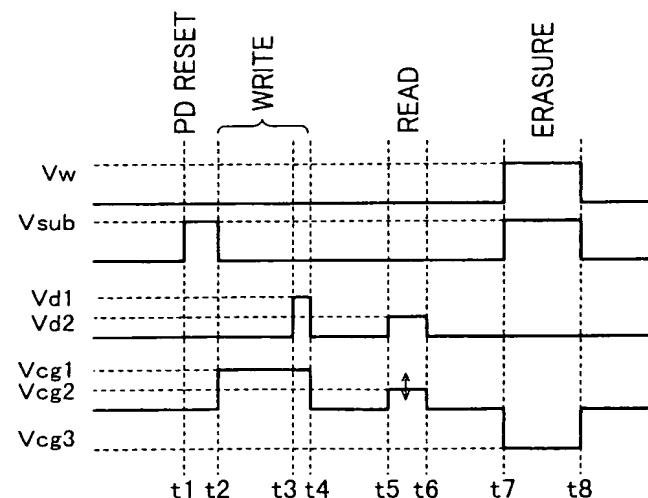
**FIG.4B**

MECHANICAL SHUTTER  
 p-TYPE WELL  
 n-TYPE SUBSTRATE  
 MEMORY ELEMENT DRAIN MD  
 MEMORY ELEMENT CONTROL GATE CG

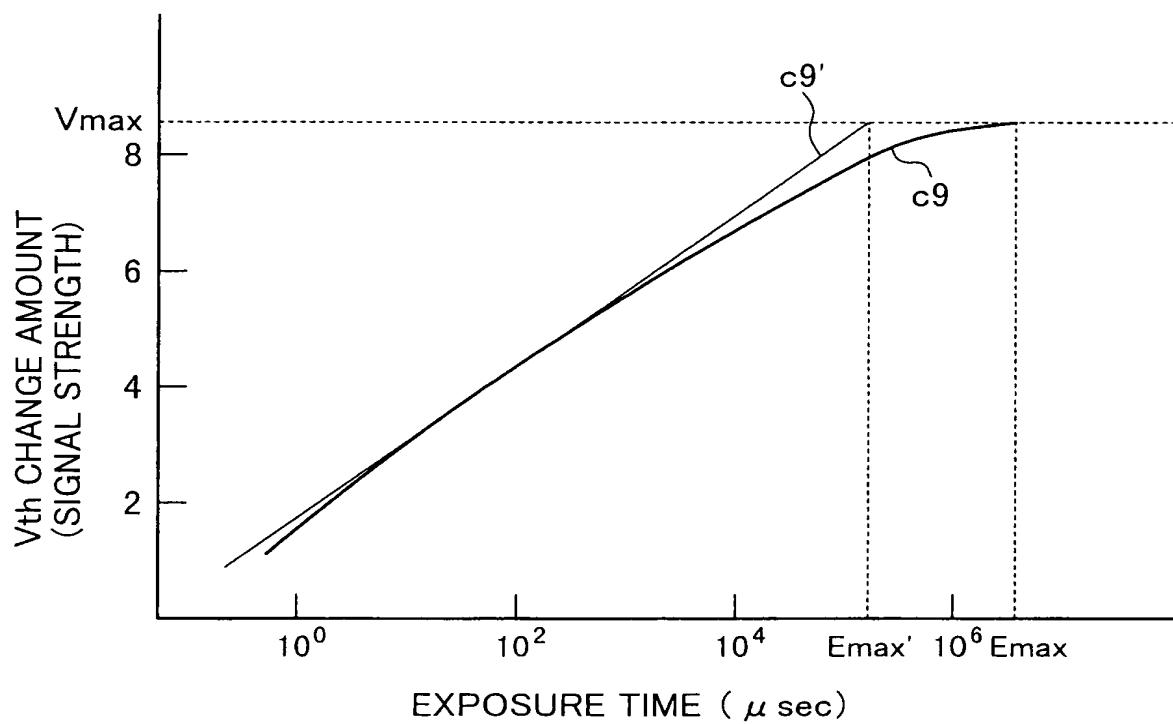


**FIG.4C**

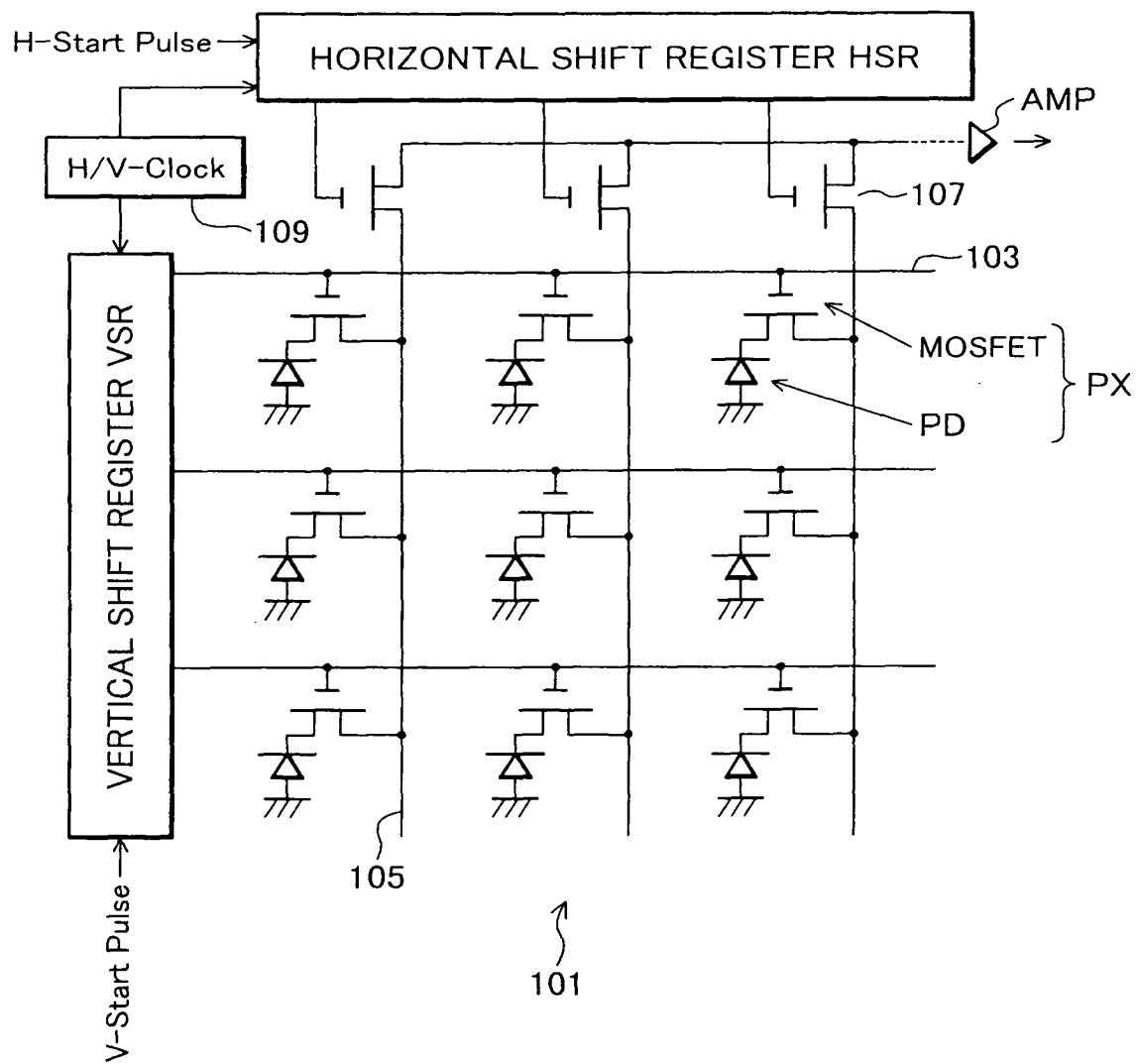
p-TYPE WELL  
 n-TYPE SUBSTRATE  
 MEMORY ELEMENT DRAIN MD  
 MEMORY ELEMENT CONTROL GATE CG



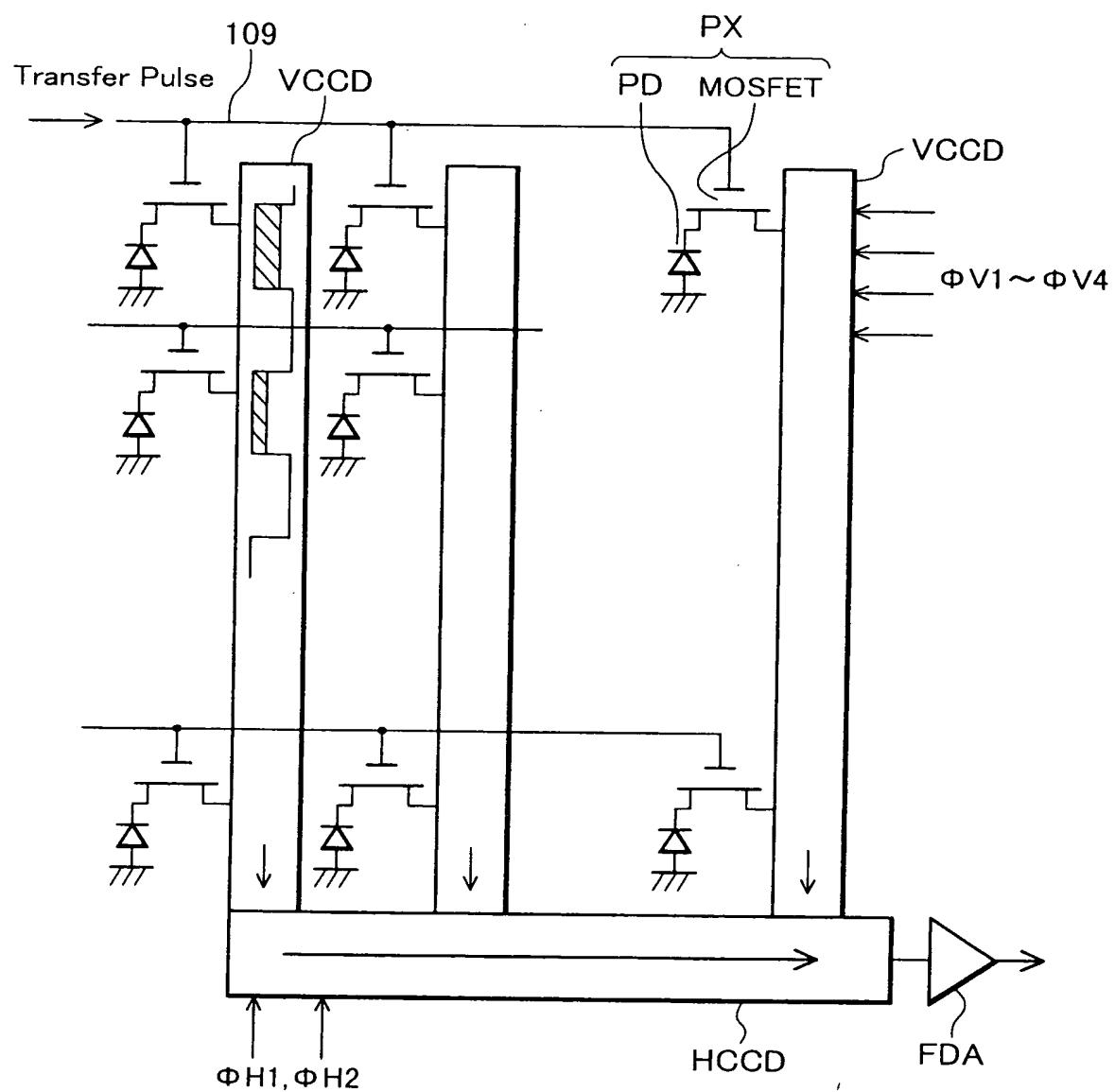
**FIG.5**



**FIG.6**  
**PRIOR ART**



**FIG.7**  
**PRIOR ART**



**FIG.8**  
**PRIOR ART**

